CHAPTER 13

Seven-watt Stereophonic Amplifier

The circuit diagram drawn in Fig. 1 is for a high-quality, dual-channel amplifier designed principally for stereophonic reproduction but also providing facilities for monaural applications. Only one channel of the amplifier is drawn. The circuitry appearing between the dotted lines is for the left-hand channel: it should be duplicated for the right-hand channel. The circuitry drawn outside the dotted lines (the power supply, for example) is common to both channels.

The total complement of valves used in the amplifier consists of one double triode, type ECC83, four triode pentodes, type ECL82, and one full-wave rectifier, type EZ81. The double triode is shared between the two channels, one section of the valve being used in each channel for voltage amplification. Two of the ECL82s are used in each channel. The triode sections of these valves form a phase-splitting stage and the pentode sections are arranged as a pushpull output stage with distributed loading. The EZ81

forms a conventional power supply with resistancecapacitance smoothing, providing the h.t. for both channels.

The rated output-power reserve of each channel is 7W, at which level the total harmonic distortion is always better than 0.5%. The low level of distortion is achieved by using 21dB of negative feedback, the feedback voltage being taken from the secondary winding of the output transformer in each channel to the cathode circuit of the corresponding input stage. The sensitivity of the circuit, even with this high value of feedback, is 100 mV, which is ample for use with existing stereophonic crystal pick-up heads.

CIRCUIT DESCRIPTION

Resistors and capacitors appearing in the left-hand channel of the amplifier are numbered 1, 2, 3, etc.; the corresponding components in the right-hand channel are numbered 101, 102, 103. etc.



Prototype of Seven-watt Stereophonic Amplifier

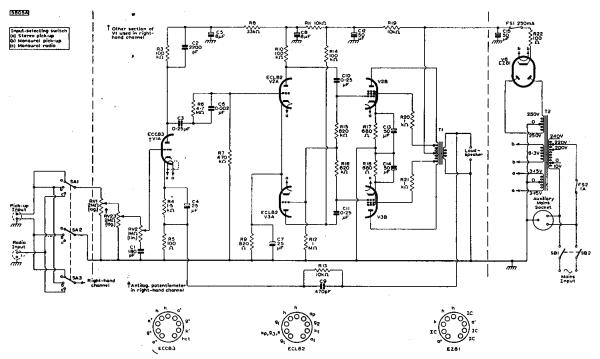


Fig. 1—Circuit diagram of seven-watt stereophonic amplifier (left-hand channel only is shown: circuitry between vertical dotted lines should be repeated in right-hand channel)

Input Selector Switch

The input stages of both channels are connected to the 3-way selector switch SA. The switch positions indicated in Fig. 1 provide the following facilities:

- (a) Stereophonic reproduction from stereophonic crystal pick-up heads.
- (b) Dual-channel monaural reproduction from a monaural pick-up head. In this position, the left-hand pick-up input terminal is 'live', and both channels are connected in parallel at the input. The input terminal for the right-hand channel of a stereophonic pick-up head is earthed at position b of SA2. If position b of SA3 is earthed instead of being connected to position b of SA1, single-channel reproduction is possible.
- (c) Dual-channel monaural reproduction from an f.m. tuner unit. The input socket in Fig. 1 is connected for monaural applications. If position c of SA3 is connected to the right-hand input terminal instead of the left-hand terminal, the circuit will be suitable for reproducing stereophonic transmissions. If position c of SA3 is earthed instead of being connected to the input socket, the system gives single-channel monaural reproduction from an f.m. tuner unit.

Input Stage

The triode sections of the ECC83 are used for voltage amplification, one section being used in each channel. A simple treble tone-control network consisting of the components RV2 and C1 (RV102 and C101) is incorporated in the grid circuit giving continuously variable treble cut. A volume control RV1 (RV101) is also included in the grid circuit. Dual-ganged potentiometers are used in the prototype for the tone-control components RV2 and RV102 and also for the volume-control components RV1 and RV101, so that equal adjustments can be made simultaneously to both channels. It may be found more convenient to use dual-concentric potentiometers for these controls because adjustments can be made separately to each channel with them.

Instability can result from the large amount of negative feedback introduced to the cathode circuit of each input stage, and the simplest method of ensuring stability is to reduce the loop gain of each channel. To achieve this reduction at high audio frequencies, the capacitor C2 (C102) is connected to bypass the anode load resistor R3 (R103). To reduce the loop gain at bass frequencies, the parallel combination R6 and C6 (R106 and C106) is included in

LIST OF COMPONENTS

Resistors and capacitors in the left-hand channel are numbered 1, 2, 3, etc.; corresponding components in the right-hand channel are numbered 101, 102, 103, etc.

	176212	1013	
Valu	ıe	Tolerance $(\pm\%)$	Rating (W)
2×2	$M\Omega$	logarithmic potenti	ometer
2×1	$M\Omega$	linear potentiomete	er
100	kΩ	10	1
	5kΩ	10	រឹ
100		10	į
	$7M\Omega$	` 10	į
470		10	Į.
	kΩ	10	į
820		10	ĩ
100	$\mathbf{k}\mathbf{\Omega}$	10	į
10	kΩ	10	į
1	$M\Omega$	10	į
10	kΩ	10	7

10

5

10

10

10

10

10

10

2 $M\Omega$ logarithmic potentiometer 2 $M\Omega$ antilogarithmic potentiometer

Capacitors

		-		
Circ	uit ref.	Value	Description	Rating (V)
C1, C2, C3, C4, C5, C6, C7, C8, C9,	C101 C102 C103 C104 C105 C106 C107 C108 C109 C110	180 pF 2200 pF 0·25 μF 25 μF 8 μF 0·002μF 25 μF 8 μF 470 pF 0·25 μF	silvered mica silvered mica paper electrolytic electrolytic paper electrolytic electrolytic silvered mica paper	275 3 300 275 3 300 275
C11, C12, C13, C14, †C1	C111 †C112 C113 C114	0·25 μF 50 μF 50 μF 50 μF 50 μF	paper electrolytic electrolytic electrolytic electrolytic	275 300 3 3 300

†(50+50+50)µF electrolytic capacitor Tolerance of silvered mica capacitors is $\pm 10\%$

Output Transformer

100 kΩ

820 kΩ

680 Ω

680

100 Ω

10 $k\Omega$

820 kΩ

kΩ

kΩ

Primary:

Circuit ref.

*RV1 and RV101 *RV2 and RV102

R103 R104 R105 R106 R107

R108

R109

R110

R112

R114

R115

R116

R117

R118

R119

R120

R121

R3, R4, R5, R6,

R8,

R9

R10.

R11, R12, R13,

R14, R15,

R16,

R17,

R18,

R19.

R20

*RV23

*RV123

*10% law (See text)

Secondary 3.75, 7.5 and 15Ω

Mains Transformer

Primary: 10-0-200-220-240V

Secondaries: H.T. 250-0-250V, 150mA L.T. 3·15-0-3·15V, 4A (for ECC83 and ECL82s)

0-6.3V, 1A (for EZ81)

ts

Manufacturer Colne	Type No.
	03062
Elden	1298
Gardners	AS.7009
Gilson	W.O.1340
Hinchley	1535
Howells	MO10
Parmeko	P2932
Partridge	P4133
Wynail	7014

Commercial Components

Manufacturer Colne	Type No. 04022
Elden	1297
Gardners	RS.3110
Gilson	W.O.741B
	W.O.741A/B
Hinchley	1447
Howells	MM10
Parmeko	P2931
Partridge	P4132
Savage	5D32
Wynali	7915

Valves

Mullard ECC83, ECL82 (four), EZ81

Valveholders

B9A (noval) nylon-loaded with screening skirt (for ECC83) McMurdo XM9/AU, skirt 95 B9A (noval) (five), McMurdo BM9/U

Miscellaneous

Mains input plug, 3-pin. Bulgin, P429 Mains switch. N.S.F., 8370/B3 Fused voltage selector. Clix, VSP/393/2 H.T. supply plug (pre-amplifier) 6-pin. Bulgin, P427 Fuseholder, Minifuse. Belling Lee, L.575 Fuse, 250mA Lampholder. Bulgin, D675/1/Red Pilot lamp. 6.3V, 0.15A., L.E.S. Input socket, 3-pin. Belling Lee, L.790/CS Output plug, (two) 2-pin, non-reversible. Painton, 313263 the coupling circuit between the first and second stages of each channel. This manner of coupling results in resistive attenuation at low audio frequencies, so that the phase shift is limited.

Balance Control

To compensate for any differences in acoustical output resulting from unequal outputs from the stereophonic pick-up head or unequal sensitivities of the loudspeakers, a balance control consisting of RV23

and RV123 is incorporated between the volume and tone controls. This is made up of a logarithmic potentiometer connected in reverse in one channel and an antilogarithmic potentiometer connected normally in the other. The characteristics of this control are discussed on page 28 in Chapter 4. If dual-concentric potentiometers are used for the volume control RV1, RV101, balance can be achieved with them, and the potentiometers RV23, RV123 can be omitted.

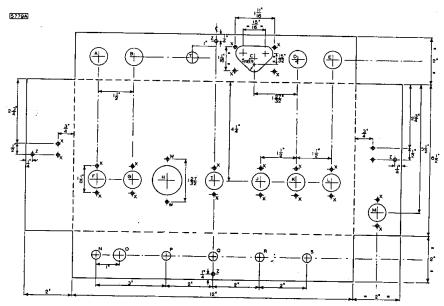


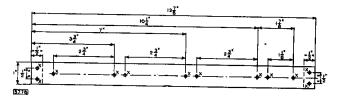
Fig. 2—Chassis details (the pieces should be bent up at 90° at all dotted lines)

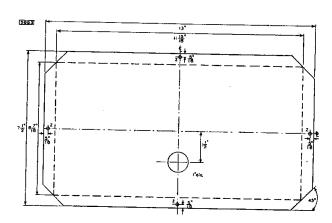
(a) (above) Main chassis

(b) (right) Tagboard mounting strip

(c) (bottom right) Cover plate

	KEY TO HOLES IN CHASSIS			
Hole	Dimension	Use	Type No.	
ABCDW#GH_ JKLMZOPQRSTXN	‡ in. dia. † in. dia.	Output socket, 2-pin. Painton Mains input plug, 3-pin. Bulgin Fused voltage selector. Clix H.T. supply plug, 6-pin. Bulgin Output socket, 2-pin. Painton B9A (noval) valveholder. McMurdo B9A (noval) valveholder. McMurdo Electrolytic capacitor B9A (noval) valveholder. McMurdo B9A (noval) valveholder. Bulgin Mains switch. N.S.F. 2×100kΩ logarithmic potentiometers 500kΩ log/antilog potentiometers 500kΩ log/antilog potentiometers Selector switch, 3-way, miniature rotary Fuseholder, Minifuse. Belling Lee 6 B.A. clearance holes	313263 P429 VSP/393/2 P427 313263 BM9/U BM9/U XM9/AU Skire 95 BM9/U BM9/U L.790/CS D675/1/Red 8370/B3	





Phase-splitting Stage

The output from the anode of V2A is taken to the grid of V3A by way of the resistors R15 and R12 (R115 and R112). These components also form the grid-leak resistance for the pentode section V2B, and R16 and R12 (R116 and R112) comprise the grid-leak resistance for V3B. Balance between the output voltages from the triodes of the phase-splitter is governed by the values of these three resistors.

Output Stage

The pentode sections of the ECL82s are used in a push-pull output stage. Distributed loading is used, and the primary winding of the output transformer is tapped so that 20% of the winding in each anode circuit also appears in the corresponding screen-grid circuit. (The choice of tapping points is discussed in Chapter 3.)

Negative Feedback

The amount of negative feedback which is used between the secondary winding of the output transformer in each channel to the cathode circuit of the input stage of that channel is 21dB. The output resistance of each channel with this feedback is 0.54Ω measured at the 15Ω output terminal. This gives an adequate damping factor of approximately 28.

Power Supply

A conventional power supply using the Mullard full-wave rectifier, type EZ81, with resistance-capacitance smoothing, provides the h.t. for both channels. Both supply connections are made to the common reservoir capacitor C15, but smoothing is achieved separately in each channel with the components R19, C12 and R119, C112.

The resistor R22 in the cathode circuit of the EZ81 should, with the transformer resistance, provide the minimum limiting resistance quoted for the valve. The choice of value for limiting resistors is discussed on page 28. A total h.t. current of 150mA at 260V is required for the amplifier, and the total heater current needed is 5A at 6.3V.

CONSTRUCTION AND ASSEMBLY

The chassis for the 7W stereophonic amplifier is made from three separate pieces of 16 s.w.g. aluminium sheet. The dimensions (in inches) of these pieces are:

(a) Main chassis $16 \times 10\frac{1}{2}$

(b) Tagboard mounting strip $12\frac{7}{8} \times 1$

(c) Cover plate $13 \times 7\frac{1}{2}$

Each piece should be marked as shown in the drawings of Fig. 2, and the holes should be cut as indicated. Where bending is required, it is important that the bends should be made accurately at the lines for the pieces to fit together properly on assembly.

Most of the smaller components should be mounted on tagboards, the wiring of which is shown in Figs. 4 to 7, and the tagboards should be fixed to the mounting strip. The larger components such as switches, valveholders and transformers should be mounted in the chassis in the positions indicated in the layout diagram of Fig. 3. The recommended wiring between these components is also indicated in Fig. 3. It will be seen that the two channels of the amplifier are separate, one channel being in each half of the chassis.

PERFORMANCE

Distortion

The total harmonic distortion was measured in the prototype amplifier with a continuous sine-wave input signal at 400c/s. With 21dB of negative feedback, the distortion for the rated output of 7W per channel

TABLE 1

D.C. Conditions in Each Channel

Point of Measurement		Voltage (V)	Range of D.C. Avometer* (V)
Comme	C15 on to both channels	260	1000
	C12 C8 C5	230 210 190	1000 1000 1000
V2, V3 ECL82	Pentode anodes Pentode screen grids Pentode cathodes Triode anode V3 Triode anode V2 Triode cathodes	256 255 21 130 110 2·0	1000 1000 100 1000 1000 25
V1 ECC83	Anode Cathode	90 1·5	100 25

*Resistance of Avometer:

1000V-range, resistance = $20M\Omega$ 100V-range, resistance = $2M\Omega$

25V-range, resistance = $500k\Omega$

7W STEREOPHONIC AMPLIFIER

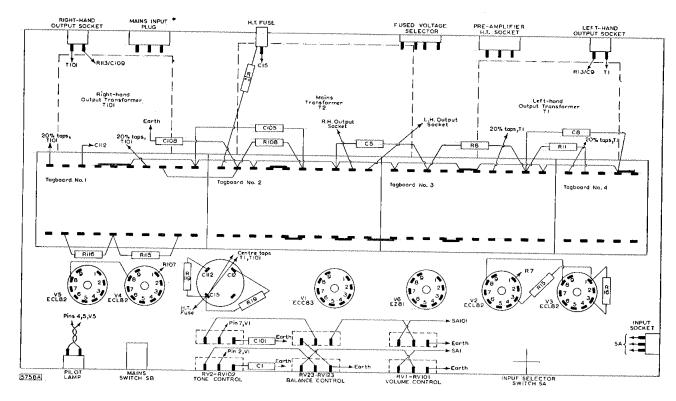
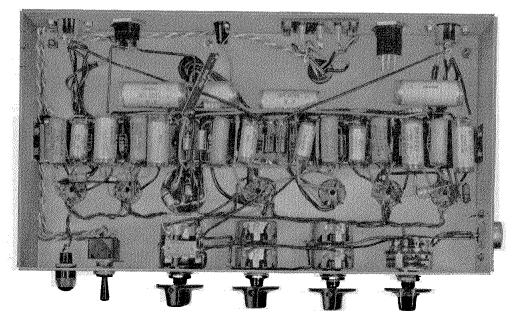
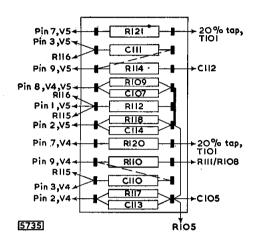


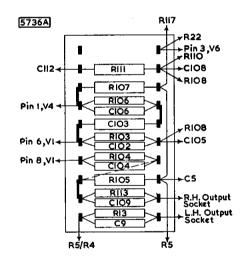
Fig. 3—Suggested layout of components

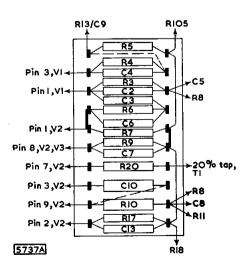


Underside View of Prototype Amplifier

7W STEREOPHONIC AMPLIFIER







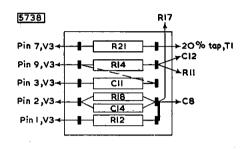


Fig. 4 (top left)—Tagboard No. 1

Fig. 5 (middle left)—Tagboard No. 2

Fig. 6 (bottom left)—Tagboard No. 3

Fig. 7 (above)—Tagboard No. 4

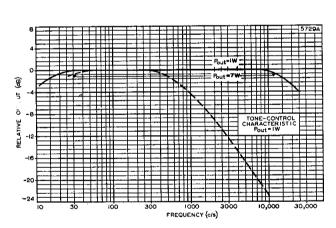


Fig. 8—Frequency-response, power-response and tonecontrol characteristics

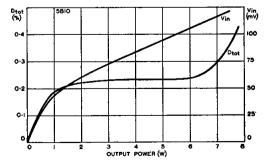


Fig. 9—Variations in harmonic distortion $D_{\rm tot}$, and input $V_{\rm in}$ with output power

7W STEREOPHONIC AMPLIFIER

is always better than 0.5%. The variation of distortion with output power is shown in Fig. 9.

Intermodulation distortion, measured with carrier and modulating frequencies of 10kc/s and 40c/s respectively, is better than 1.5%.

Frequency Response

The frequency-response characteristic of each channel of the prototype amplifier, measured for an output of 1W is shown in Fig. 8. At 20kc/s, the response is 3dB below the level at 1kc/s and at 15c/s it is 1.5dB down. The response at the rated output power of 7W is 3dB down at 15c/s.

Sensitivity

The sensitivity of each channel of the circuit (including the controls) with 21dB of negative feedback is 100mV for the rated output of 7W.

Hum and Noise

The level of hum and noise in each channel of the prototype amplifier is better than 65dB below the rated output.

Tone Controls

A continuously variable treble-cut tone control is included in each channel of the amplifier. The characteristic of this control is shown in Fig. 8, from which it can be seen that full application of the control gives about 23dB of cut at 10kc/s.

D.C. CONDITIONS

The d.c. voltages in each channel should be tested with reference to Table 1. The results shown in this table were obtained with an Avometer No. 8.